United States District Court,
D. Delaware.

MOSEL VITELIC CORP,
Plaintiff.
v.
MICRON TECHNOLOGY, INC,
Defendant.
Micron Technology, Inc,
Counter-Plaintiff.
v.
Mosel Vitelic Corp. and Mosel Vitelic, Inc,
Counter-Defendants.

Civil Action No. 98-449-GMS

Feb. 15, 2000.

Named Expert: N. Elton Dry
Lewis H. Lazarus, Morris James LLP, Richard D. Kirk, Bayard, P.A., Wilmington, DE, for Plaintiff.

Richard K. Herrmann, Morris James LLP, Wilmington, DE, for Defendant.

ORDER CONSTRUING THE DISPUTED TERMS OF U.S. Patent No. 5,481,581

GREGORY M. SLEET**, District Judge.**

After considering the submissions of the parties and hearing oral argument on the matter, IT IS HEREBY ORDERED, ADJUDGED, and DECREED that, as used in the asserted claims of U.S. Patent No. 5,481,581,

1. The term "count stage," in claims 13, 17 and 18, means "a circuit that provides a count bit;"

2. The term "another source of control signals for the count stages," in claim 13, means "further circuitry providing two or more signals to control the count stages;"

3. The term "further count sequence for use as control signals for the count stages," in claim 14, means the "count sequence generated by the further circuitry providing the count signals to the count stages;"

4. The term "lowest order bit," in claim 17, means the "count bit generated by the lowest order count stage;"

5. The term "second lowest order bit," in claim 17, means the "count bit generated by the second lowest order count stage;" and

6. The term "third lowest order bit," in claim 17, means the "count bit generated by the third lowest order count stage."

D.Del.,2000.
Mosel Vitelic Corp. v. Micron Technology, Inc.